Version With Markings To Show Changes Made

IN THE CLAIMS:

1. (TWICE AMENDED) A data transfer arrangement comprising:

two bus drivers;

a voltage precharge source;

a differential bus coupled to the bus drivers and to the voltage precharge source; and

a latching sense amplifier coupled to the differential bus;

wherein the latching sense amplifier comprises:

a first stage including a cross-coupled latch coupled to a differential data bus; and

an output stage coupled to an output of said first stage;

wherein the output of the first stage is coupled to an input of the output stage[.];

wherein the differential bus and the differential data bus are precharged to a voltage Vpr between Vdd and ground, where Vpr = K*Vdd, and K is a precharging voltage factor.

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